

# Angus Pearson

University of Edinburgh Undergraduate

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Web & Portfolio: <http://toaster.cc>

GitHub: <https://github.com/AngusP>

LinkedIn: <https://uk.linkedin.com/in/anguspearson>

Nationality: British and Australian

## Education

2013-2017 BSc (Hons) Computer Science & Artificial Intelligence, University of Edinburgh

*Graduated with First Class, Honours*

2011-2013 Pangbourne College (A Levels, GCSEs)

## Work & Experience

Jun 2016 - present CTO, Robotical Ltd.  
*Work focuses primarily on the online presence, shopfront and API/Integrations, but also on the electronics for Marty the Robot.*  
<https://robotical.io/>

Sept 2013 - present Tardis Project Systems Administrator  
*The project is a long running student-led systems and network administration 'sandbox' with roughly 760 registered users. Administration involves hardware management, virtualisation, networking, service deployment and maintenance. Since beginning work on the project a full migration onto new hardware has been completed.*  
<http://wiki.tardis.ed.ac.uk>

Jan 2017 - May 2017 System Design Project Mentor/Demonstrator  
*The project involves building a wheeled robot, vision system and strategic control system to play robot football, loosely based on the Robocup Small Size League. Mentoring involves guiding a SDP team through the course, checking on their progress and team management. (Teaching support role) SDP is a University course, I took the course in 2016.*  
<http://www.inf.ed.ac.uk/teaching/courses/sdp/>

Dec 2014 - Jun 2015 Full Stack Web Freelance, GPB Consulting Ltd. (Content Analysis Web Application)  
*Development of a Natural Language processing (NLTK) and Python backend, deployed on Heroku. The web app provides a set of market-leading measures of written content readability and complexity, used by the company in providing reports for clients. It is to my knowledge the most complete collection of these metrics on the web.*  
<http://ca.gpb.eu>

- Sept 2014 -  
May 2015      CompSoc President (Edinburgh University Computing Society)  
*This position included the organisation of two large hackathons, 'Hack the Burgh' (Major League Hacking) and 'The Smart Data Hack', as well as event management, financial management, logistics, marketing and sponsorship negotiation with companies including Google, Microsoft, Amazon DCS, Bloomberg and Accenture.*  
<http://comp-soc.com> – <http://hacktheburgh.com> – <http://smartdatahack.org>
- 2014-2015      Edinsolar - University of Edinburgh Solar Car Project Firmware team lead  
*Initially I was on the Web Team and developed the frontend of the project website. Roles included firmware team management, co-operation with the Electrical and Business teams, developing firmware for the vehicle's control systems, and development of any design or logistical software required.*  
<http://edinsolar.github.io/> – <https://github.com/EdinSolar>
- Jun 2014 -  
Sept 2014      Front-end Web Freelance, Diamond Minding Ltd.  
*I developed a website for the company to help them grow their business and present their services. Note that the site has been handed over but is undeployed as the company is not currently operating.*  
<http://diamondminding.github.io>
- 2012              Arkwright Scholar at Stannah Lifts Group  
*The scholarship included a 2 week internship where I developed a MP3 backed electronic sounder for a lift, that allowed sound profiles to be easily loaded via a SD card. This extended the functionality of the existing system which only produced a couple of tones to indicate state.*  
<http://www.arkwright.org.uk/>
- 2011-2012      Frontend Web Freelance, GPB Consulting Ltd. (Main Company Site)  
*Development of a WordPress site for the company, with some extended functionality to handle monthly Journal publications.*  
<http://www.gpb.eu>

## Full Project Work and Portfolio

<http://toaster.cc/portfolio>

## Relevant Skills

### PROGRAMMING LANGUAGES

Python, C, HTML5, CSS3

*Confident, multiple coursework assignments and own projects*

C++, Haskell, JavaScript

*Moderate experience, multiple coursework assignments or previous work*

### ELECTRONICS AND HARDWARE

Digital Electronics circuit design, small-pitch and multi-layer PCB layout.

Experience with Verilog (Hardware Description Language)

Linux Systems Administration.

## Extracurricular

Completed the DofE Gold and Bronze awards.

## Contact

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